

(use as many sheets as necessary)

Application Number	10/750,420
Filing Date	December 31, 2003
First Named Inventor	Yan Zhou
Group Art Unit	2616
Examiner Name	Chin, Wellington
Attorney Docket Number	75622P006301

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Examiner Initials ¹	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MMM-DD-YYYY	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear
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¹Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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[illegible][illegible]

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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
Re		INFINEON TECHNOLOGIES, "Product Brief: GEMINAX Global Enhanced Multiport Integrated ADSL Transceiver", Infineon Technologies AG, Germany (2001)	
Re		INFINEON TECHNOLOGIES, "Product Brief: GEMINAX ADSL Transceiver Chipset", Infineon Technologies AG, Germany (2001)	
Re		INFINEON TECHNOLOGIES, "Press Release: Infineon Technologies Introduces the IVAX Family - the First Chipset to Integrate Voice and Fullrate ADSL on a Single Line Card", Infineon Technologies AG, Germany (May 15, 2001)	
Re		PESSL, PETER, ET AL. "A four channel Integrated Voice and ADSL Full-Rate Analog Front End", 27th European Solid-State Circuits Conference, Villach, Austria, September 18-20, 2001	

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